

WHAT IS CLAIMED IS:

1. An amplifier comprising:

a first pair of active devices having common terminals thereof coupled together, wherein a control terminal of a first device of the first pair forms a first type non-inverting amplifier input port and an output terminal thereof forms an inverting amplifier output port;

wherein a control terminal of a second device of the first pair forms a first type inverting amplifier input port;

a second pair of active devices having common terminals thereof coupled together, wherein a control terminal of a first device of the second pair is coupled to the control terminal of the second device of the first pair, wherein an output terminal of a second device of the second pair forms a non-inverting amplifier output port, and wherein a control terminal of the second device of the second pair is coupled to the control terminal of the first device of the first pair; and

a third pair of active devices having common terminals thereof coupled together, wherein a control terminal of a first device of the third pair forms a second type non-inverting input port and an output terminal thereof is coupled to the common terminal of the first device of the first pair;

wherein a control terminal of a second device of the third pair forms a second type inverting input port and a common terminal thereof is coupled to the common terminal of the first device of the second pair.

2. The amplifier of claim 1, wherein the active devices are transistors.

3. The amplifier of claim 2, wherein the control terminal, the output terminal, and the common terminal are respectively a gate, a drain, and a source.

4. The amplifier of claim 1, wherein all of the impedance devices are resistors.